

Electronic Filing System (EFS) Data Electronic Patent Application Submission USPTO Use Only

EFS ID:

56635

Application ID:

Title of Invention:

10619635

Content Addressable Memory

(CAM) Devices with Dual-

Function Check Bit Cells that

Support Column Redundancy

and Check Bit Cells with Reduced

Susceptibility to Soft Errors

First Named Inventor:

Kee Park

Domestic/Foreign Application:

Domestic Application

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1230

Attorney Docket Number:

5646-91XX

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TRANSMITTAL

Electronic Version v1.1
Stylesheet Version v1.1.0

Title of Invention Content Addressable Memory (CAM) Devices with Dual-Function Check Bit Cells that Support Column Redundancy and Check Bit Cells with Reduced Susceptibility to Soft Errors

Application Number:

10/619635

Date:

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First Named Applicant:

Kee Park

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Submitted by:	Elec. Sign.	Sign. Capacity
Grant J. Scott Registered Number: 36925	/gjs/	Attorney

Documents being submitted

Files

us-ids

5646-91XX-usidst.xml

us-ids.dtd

us-ids.xsl

Comments



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
Stylesheet Version v18.0

Title of Invention

Content Addressable Memory (CAM) Devices with Dual-Function Check Bit Cells that Support Column Redundancy and Check Bit Cells with Reduced Susceptibility to Soft Errors

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Kee Park

Attorney Docket Number: 5646-91XX

Art Unit: .

2818

Search string:

(6700810 or 6690595 or 6618281 or

6597595).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6700810	2004-03-02	Chiriu et a.	B1	365	49
	2	6690595	2004-02-10	Srinivasan et al.	B1	365	49
	3	6618281	2003-09-09	Gordon	B1	365	49
	4	6597595	2003-07-22	Ichiriu et al.	B1	365	49

Signature

Examiner Name	Date